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"content addressable memory" +physical +virtual Search

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Formal Verification of Content Addressable Memories Using Symbolic Trajectory Evaluation

M Pandey, R Raimi, RE Bryant, MS Abadir - DAC, 1997 - doi.ieeecomputersociety.org

... Figure 1: **Content Addressable Memory**: Tag size = t, Number of entries = n, Data size = d the CAM with a symbolic ternary function, $f_k(i, tag)$, which returns ...

Cited by 19 - [Web Search](#) - [portal.acm.org](#) - [sigda.org](#) - [ieeexplore.ieee.org](#) - [all 8 versions](#) »

An ASIC-Embedded **Content Addressable Memory** with Power-Saving and Design for Test Features

T Chadwick, T Gordon, R Nadkarni, J Rowland - Proc. IEEE Custom Integrated Circuits Conf.(CICC), 2001 - [ieeexplore.ieee.org](#)

... response to this trend, the embedded **Content Addressable Memory** (CAM) de ... pre-compare bits in the **physical** memory array ... states, will serve as **virtual** ground nodes ...

Cited by 3 - [Web Search](#) - [ieeexplore.ieee.org](#)

Executing compressed programs on an embedded RISC architecture

A Wolfe, A Chanin - Proceedings of the 25th annual international symposium on ..., 1992 - [ieeexplore.ieee.org](#)

... used in **virtual** memory systems. With the CLB and LAT, full address coherency is maintained between the processor addressing model and the **physical** compressed ...

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Cascading content-addressable memories

T Moors, A Cantoni - IEEE Micro, 1992 - doi.ieeecomputersociety.org

... Similarly, combining **content-addressable memory** (CAM) devices to form a ... used for switching (for example, **virtual** path or ... a) and within a single **physical** CAM, (b ...

Cited by 6 - [Web Search](#) - [doi.ieeeecs.org](#) - [ieeexplore.ieee.org](#) - [portal.acm.org](#) - [all 6 versions](#) »

Concurrent Error Detection/Correction in the HAL MMU Chip

DCW Chang, NR Saxena, HALC Syst, CA Campbell - FTCS, 1993 - [ieeexplore.ieee.org](#)

... **Content Addressable Memory** (CAM), MMU. 1: Introduction In a **virtual** memory environment, reliable translation from **virtual** addresses to **physical** addresses is ...

Cited by 4 - [Web Search](#) - [ieeexplore.ieee.org](#)

The Biological Basis of the Immune System as a Model for Intelligent Agents

RL King, AB Lambert, SH Russ, DS Reese - IPPS/SPDP Workshops, 1999 - [ipdps.eece.unm.edu](#)

... memory may best be described as a **content addressable memory** (CAM ... operating system, internal memory available (both **physical** and **virtual**), page information ...

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Empirical study of memory-data: characteristics and compressibility

M Kjelso, M Gooch, S Jones - IEE PROC COMPUT DIGITAL TECH, 1998 - [ieeexplore.ieee.org](#)

... APPEL, AW, and LI, K.: '**Virtual** memory primitives ... compression to extend **physical** memory ... data compressor designs using **content addressable memory**', IEE Proc ...

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Dynamic Prefetching in the **Virtual** Memory Window of Portable Reconfigurable Coprocessors

M Vuletic, L Pozzi, P lenne - Proceedings of the 14th International Conference on Field- ... - [springerlink.com](#)

... image hiding all details about the **physical** memory. ... and schedule prefetch-based loads of **virtual** memory pages ... On a hit in the **Content Addressable Memory** (CAM), 1 ...

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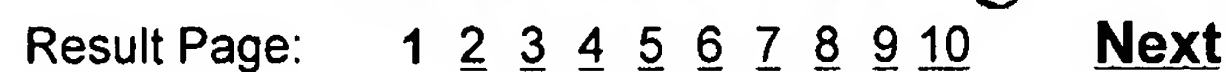
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DW Bradley, AM Tyrrell - ICES, 2000 - springerlink.com

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